

U.S. **UTILITY** Patent Application**PATENT DATE**

SCANNED

Q.A

APPLICANTS

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TITLE

Clock synchronization circuit and method

PTO-2040
12/89

ISSUING CLASSIFICATION

ORIGINAL						CROSS REFERENCE(S)						
CLASS				SUBCLASS		CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____ _____	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	_____ (Assistant Examiner) (Date) _____ (Primary Examiner) (Date) _____ (Legal Instruments Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
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